

The following tables list the screw terminal assignments for each DT9850 Series module:

- DT9853 module – [Table 1 on page 2](#)
- DT9853-M module – [Table 2 on page 3](#)
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Table 1: DT9853 Screw Terminal Assignments

Screw Terminal	Signal	Screw Terminal	Signal
20	+5 V Out	40	Digital Output, Line 7
19	Digital I/O Termination Select	39	Digital Output, Line 6
18	Analog Ground	38	Digital Output, Line 5
17	Reserved	37	Digital Output, Line 4
16	Reserved	36	Digital Output, Line 3
15	Reserved	35	Digital Output, Line 2
14	Reserved	34	Digital Output, Line 1
13	Reserved	33	Digital Output, Line 0
12	Reserved	32	Digital Ground
11	Reserved	31	Digital Input, Line 7
10	Reserved	30	Digital Input, Line 6 ^a
9	Analog Ground	29	Digital Input, Line 5 ^a
8	Reserved	28	Digital Input, Line 4 ^a
7	Analog V_Output 3	27	Digital Input, Line 3 ^a
6	Reserved	26	Digital Input, Line 2 ^a
5	Analog V_Output 2	25	Digital Input, Line 1 ^a
4	Reserved	24	Digital Input, Line 0 ^a
3	Analog V_Output 1	23	Digital Ground
2	Reserved	22	DAC_Sync
1	Analog V_Output 0	21	Counter Input

a. Supports interrupt-on-change.

Table 2: DT9853-M Screw Terminal Assignments

Screw Terminal	Signal	Screw Terminal	Signal
20	+5 V Out	40	Digital Output, Line 7
19	Digital I/O Termination Select	39	Digital Output, Line 6
18	Analog Ground	38	Digital Output, Line 5
17	Reserved	37	Digital Output, Line 4
16	Reserved	36	Digital Output, Line 3
15	Reserved	35	Digital Output, Line 2
14	Reserved	34	Digital Output, Line 1
13	Reserved	33	Digital Output, Line 0
12	Reserved	32	Digital Ground
11	Reserved	31	Digital Input, Line 7
10	Reserved	30	Digital Input, Line 6 ^a
9	Analog Ground	29	Digital Input, Line 5 ^a
8	Analog I_Output 3	28	Digital Input, Line 4 ^a
7	Analog V_Output 3	27	Digital Input, Line 3 ^a
6	Analog I_Output 2	26	Digital Input, Line 2 ^a
5	Analog V_Output 2	25	Digital Input, Line 1 ^a
4	Analog I_Output 1	24	Digital Input, Line 0 ^a
3	Analog V_Output 1	23	Digital Ground
2	Analog I_Output 0	22	DAC_Sync
1	Analog V_Output 0	21	Counter Input

a. Supports interrupt-on-change.

Table 3: DT9854 Screw Terminal Assignments

Screw Terminal	Signal	Screw Terminal	Signal
20	+5 V Out	40	Digital Output, Line 7
19	Digital I/O Termination Select	39	Digital Output, Line 6
18	Analog Ground	38	Digital Output, Line 5
17	Reserved	37	Digital Output, Line 4
16	Analog V_Output 7	36	Digital Output, Line 3
15	Reserved	35	Digital Output, Line 2
14	Analog V_Output 6	34	Digital Output, Line 1
13	Reserved	33	Digital Output, Line 0
12	Analog V_Output 5	32	Digital Ground
11	Reserved	31	Digital Input, Line 7
10	Analog V_Output 4	30	Digital Input, Line 6 ^a
9	Analog Ground	29	Digital Input, Line 5 ^a
8	Reserved	28	Digital Input, Line 4 ^a
7	Analog V_Output 3	27	Digital Input, Line 3 ^a
6	Reserved	26	Digital Input, Line 2 ^a
5	Analog V_Output 2	25	Digital Input, Line 1 ^a
4	Reserved	24	Digital Input, Line 0 ^a
3	Analog V_Output 1	23	Digital Ground
2	Reserved	22	DAC_Sync
1	Analog V_Output 0	21	Counter Input

a. Supports interrupt-on-change.

Table 4: DT9854-M Screw Terminal Assignments

Screw Terminal	Signal	Screw Terminal	Signal
20	+5 V Out	40	Digital Output, Line 7
19	Digital I/O Termination Select	39	Digital Output, Line 6
18	Analog Ground	38	Digital Output, Line 5
17	Analog I_Output 7	37	Digital Output, Line 4
16	Analog V_Output 7	36	Digital Output, Line 3
15	Analog I_Output 6	35	Digital Output, Line 2
14	Analog V_Output 6	34	Digital Output, Line 1
13	Analog I_Output 5	33	Digital Output, Line 0
12	Analog V_Output 5	32	Digital Ground
11	Analog I_Output 4	31	Digital Input, Line 7
10	Analog V_Output 4	30	Digital Input, Line 6 ^a
9	Analog Ground	29	Digital Input, Line 5 ^a
8	Analog I_Output 3	28	Digital Input, Line 4 ^a
7	Analog V_Output 3	27	Digital Input, Line 3 ^a
6	Analog I_Output 2	26	Digital Input, Line 2 ^a
5	Analog V_Output 2	25	Digital Input, Line 1 ^a
4	Analog I_Output 1	24	Digital Input, Line 0 ^a
3	Analog V_Output 1	23	Digital Ground
2	Analog I_Output 0	22	DAC_Sync
1	Analog V_Output 0	21	Counter Input

a. Supports interrupt-on-change.

